



RECEIPT

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Tan et al.

**Serial No.:** 10/645,389

**Filed:** August 21, 2003

**For:** APPARATUS, SYSTEMS AND  
METHODS RELATING TO THE  
RECONSTRUCTION OF  
SEMICONDUCTOR WAFERS FOR  
WAFER-LEVEL PROCESSING AND  
RECONSTRUCTED SEMICONDUCTOR  
WAFERS

**Confirmation No.:** 8099

**Examiner:**

**Group Art Unit:** 2812

**Attorney Docket No.:** 2269-5528US

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 10, 2003  
Date

Signature

Shirley Dougherty

Name (Type/Print)

**REQUEST FOR CORRECTED FILING RECEIPT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the official filing receipt received from the PTO in the above application. Issuance of a corrected filing receipt in accordance with the annotations on the attached filing receipt copy is respectfully requested.

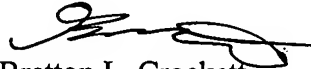
The errors to be corrected are as follows:

The applicants are Yong Kian Tan and Wu Yean Tay.

Serial No.: 10/645,389

These corrections are not due to any errors by Applicants and no fee is due.

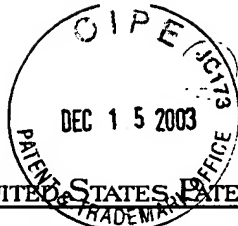
Respectfully submitted,



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Date: December 10, 2003  
BLC/sdd

Enclosure: Copy of Official Filing Receipt with annotations correcting errors  
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/645,389	08/21/2003	2812	1476	5528US (02-1052.00/US)	10	51	5

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**Trask Britt**

CONFIRMATION NO. 8099

FILING RECEIPT



\*OC000000011284925\*

24247  
 TRASK BRITT  
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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

~~Kian Tan Yong~~, Residence Not Provided;  
 YONG KIAN TAN  
 WUU YEAN TAY

**Domestic Priority data as claimed by applicant****Foreign Applications**

SINGAPORE 200302784-4 07/05/2003

**If Required, Foreign Filing License Granted: 11/15/2003****Projected Publication Date:** To Be Determined - pending completion of Missing Parts**Non-Publication Request:** No**Early Publication Request:** No**Title**

Apparatus, systems and methods relating to the reconstruction of semiconductor wafers for wafer-level processing and reconstructed semiconductor wafers

**Preliminary Class**

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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